

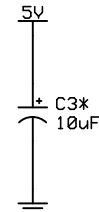
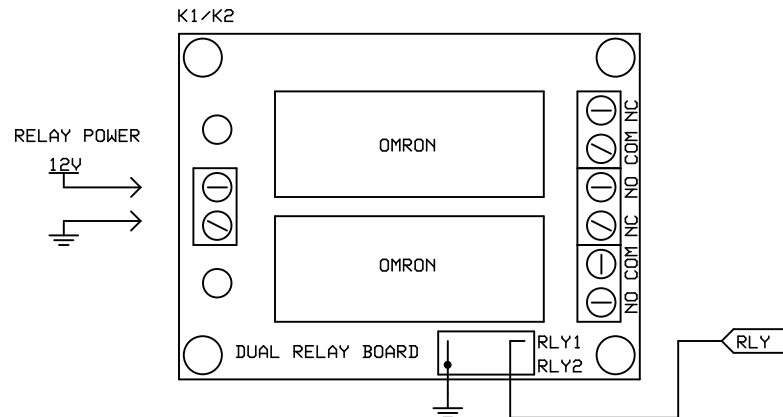
- NOTES:
- (1) PIR - PARALLAX (#910-28027) OR (#555-28027)
 - (1) U1 - CMOS TIMER IC (#604-00009)
 - (2) Q1, Q3 - 2N3904 NPN TRANSISTOR (#500-00001)
 - (1) Q2 - 2N3906 PNP TRANSISTOR (#500-00003)
 - (1) R1 - 100K, 1/4W Resistor (#150-01040)
 - (1) R2 - 10K, 1/4W Resistor (#150-01030)
 - (2) R3-R4 - 1K, 1/4W Resistor (#150-01020)
 - (1) C1 - 1000uF Capacitor (#201-01085)
 - (1) C2 - 0.01uF Capacitor (#200-01031)
 - (1) C3 - 10uF Capacitor (#201-01062)

(1) K1/K2 - DUAL RELAY BOARD KIT (#27114)

* - POWER SUPPLY FILTER CAP

R1/C1 VALUES YIELD ~2 MINUTES DELAY TIME

REPLACE R1 WITH 1M POT FOR ADJUSTABLE TIME DELAY



PIR Triggered Dual Relay Board

| | | |
|------------|------------------------|-------------|
| Rev A | Drawn By Chris Savage | Page 1 of 1 |
| 10/27/2013 | www.savagecircuits.com | |