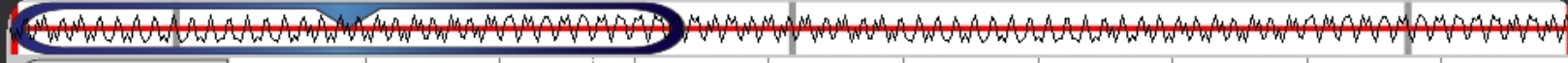


# Logic State Analyzer



At 0.05s: io=A889A6FA

Bit

i0

Pattern

i1

i2

i3

40

80

120

160

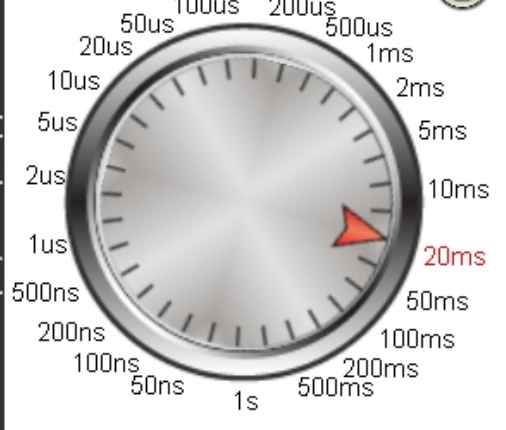
200

Time (msec)

Run



Horizontal



Click to run/stop measurements

