



Autoroute with a grid of 10mil  
 Components on board with a grid of 0.025  
 Design rule check min track width 10mil  
 Design rule check min pad to via 40mil

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- Data transfers all one word at a time
- Steps to transfer data from ram to IL9325
- 1) assume 245 isolate is HZ
  - 2) Set read to low (data appears on local bus to IL9325)
  - 3) Set 138 output 1 low (373 latches pass data straight through)
  - 4) load in 10 bit address to P0-P18
  - 5) Toggle P22 = ILI write pin
  - 6) Increment address and goto 5
  - 7) when finished, set read to high and deselect 373 latches as pass through
- Steps to read a block of ram to the propeller
- Assumes 245 is in isolate mode
- 1) Set mem read to low via latch and set isolate direction as A<B
  - 2) set up address and toggle the 373 to latch in address
  - 3) P23 = 245 isolate low, read data on P0-P15, 245 isolate high to read data into the propeller
  - 4) increment address and goto 2
  - 5) set mem read to high via latch
- Steps to write a block from propeller to ram
- 1) set up address and toggle the 373 to load into address latches
  - 2) Set isolate direction as B<A
  - 3) Set isolate HZ low
  - 4) put data on P0-P15 and toggle memory write pin low then high
  - 5) Set isolate HZ high
  - 6) increment the address latch and toggle into 373
  - 7) goto 3