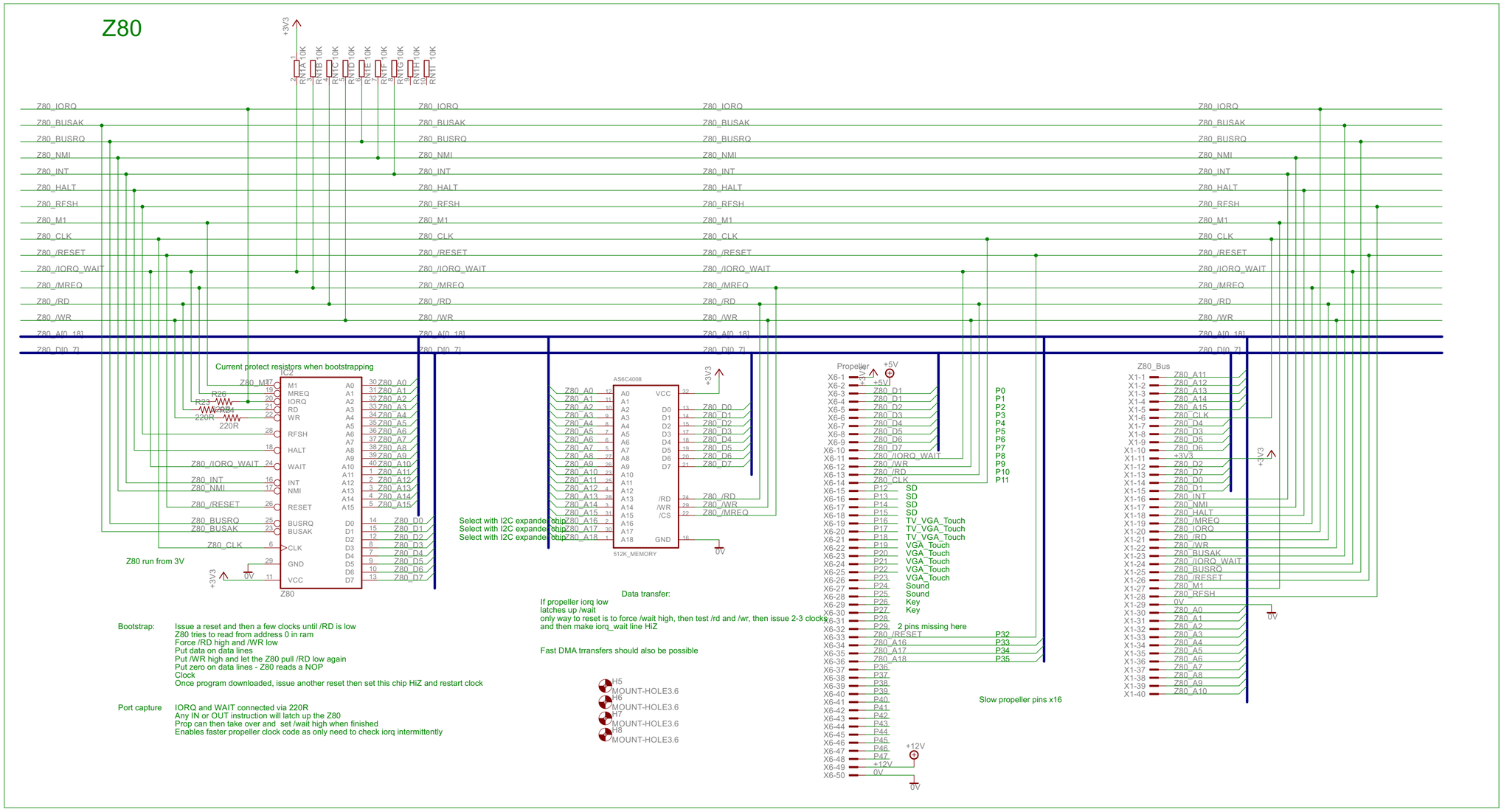
Z80



Bootstrap:
- Issue a reset and then a few clocks until /RD is low
- Z80 tries to read from address 0 in ram
- Force /RD high and /WR low
- Put data on data lines
- Put /WR high and let the Z80 pull /RD low again
- Put zero on data lines - Z80 reads a NOP
- Clock
- Once program downloaded, issue another reset then set this chip HiZ and restart clock

Port capture
- IORQ and WAIT connected via 220R
- Any IN or OUT instruction will latch up the Z80
- Prop can then take over and set /wait high when finished
- Enables faster propeller clock code as only need to check iorq intermittently

Data transfer:
- If propeller iorq low latches up /wait
- only way to reset is to force /wait high, then test /rd and /wr, then issue 2-3 clocks and then make iorq_wait line HiZ

Fast DMA trransfers should also be possible