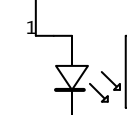


0 V 5 V  
0.5msec 1msec  
Basic Stamp I/O Pin



R1  
3.3k $\Omega$



U1  
4N33

Q1  
2N3906

R2  
470 $\Omega$

D1  
1N4001

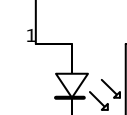
VDD  
12V

A

0 V 5 V  
0.5msec 1msec  
Basic Stamp I/O Pin



R3  
3.3k $\Omega$



U2  
4N33

Q2  
2N3906

R4  
470 $\Omega$

D2  
1N4001

VDD  
12V

B