



TO PROP CHIP 7-215079-8

Receiver Output (RO/RX) J1-1  
 Recvr Output En/Fa (RE) J1-2  
 Driver Enable Input (DE) J1-3  
 Driver Input (DI/TX) J1-4  
 Master Detect (MS) J1-5  
 GND J1-6  
 5V J1-7

CABLE CONNECTION - LOGIC TABLE

RS485 OUT	RS485 IN	TERMINATE	MASTER
EMPTY	EMPTY	DISABLED	LOW
CONNECTED	CONNECTED	DISABLED	LOW
CONNECTED	EMPTY	ENABLED	HIGH
EMPTY	CONNECTED	ENABLED	LOW

AUTO TERMINATION - GALVANIC ISOLATED - MASTER/SLAVE DETECT BY THIS SCHEMATIC IS NOT TESTED!!!

TITLE: RS485-INTERFACE BOARD REV-Q

Document Number: \_\_\_\_\_ REV: \_\_\_\_\_

Date: 18/02/2011 8:16 PM Sheet: 1/1