#### **DUAL H-BRIDGE MOTOR DRIVERS**

# UDN2993B LOAD SUPPLY 1 ENABLE A 2 PHASEA 3 GROUND 4 GROUND 5 OUT 1A 6 OUT 2A 7 VEA 8

Dwg. No. A-12,455

## ABSOLUTE MAXIMUM RATINGS at $T_1 \le +150^{\circ}C$

Load Supply Voltage, V <sub>BB</sub> 30 V
Logic Supply Voltage, V <sub>DD</sub>
Logic Input Voltage Range, V <sub>PHASE</sub> or
V <sub>ENABLE</sub> 0.3 V to V <sub>DD</sub> + 0.3 V
Output Current, I <sub>OUT</sub> ±600 mA
Sink Driver Emitter Voltage,
V <sub>E</sub> 1.5 V
Package Power Dissipation,
P <sub>D</sub> See Graph
Operating Temperature Range

T<sub>A</sub> .....-20°C to +85°C Storage Temperature Range,

T<sub>S</sub> ...... -55°C to +150°C

IMPORTANT: Load supply voltage must never be applied without logic supply voltage present.

NOTE: Output current rating may be limited by chopping frequency, ambient temperature, airflow, and heat sinking. Under any set of conditions, do not exceed the specified maximum current and a junction temperature of +150°C.

Cost-effective monolithic drive electronics for bipolar stepper and dc (brush) servo motors to 30 V and 500 mA is very practical with the UDN2993B and UDN2993LB. These dual full-bridge motion control ICs integrate separate inputs, level shifting for upper power outputs, control logic, integral inductive transient protection, and source (upper) and sink (lower) drivers in an H-bridge configuration. The single-chip power IC provides improved space utilization and reliability unmatched by discrete component circuitry.

Excepting the power supply connections, the two H-bridges are independent. An ENABLE input is provided for each bridge and permits pulse-width modulation (PWM) through the use of external circuitry. PWM drive techniques provide the benefits of reduced power dissipation, improved motor performance (especially torque), and positively affect system efficiency. Separate PHASE inputs for each bridge determine the direction of current flow in the load. Additionally, each pair of (sink) emitters are terminated to package connections. This allows the use of current-sensing circuitry. Both devices incorporate an intrinsic "dead time" to preclude high crossover or cross-conduction) currents during changes in direction (phase).

These devices are packaged in plastic DIPs (suffix B) or surface-mountable wide-body SOICs (suffix LB) with copper lead frames for optimum power dissipation without heat sinks. The lead configurations allow automatic insertion, fit standard IC sockets or printed wiring board layouts, and enable easy attachment of a heat sink for maximum power-handling capability. The heat-sink tabs are at ground potential and require no insulation.

Dual full-bridge drivers with peak current ratings of  $\pm 3$  A are supplied as the UDN2998W.

#### **FEATURES**

- ±600 mA Output Current
- Output Voltage to 30 V
- Crossover Current Protection
- TTL/NMOS/CMOS Compatible Inputs
- Low Input Current
- Internal Clamp Diodes
- Automotive Capable

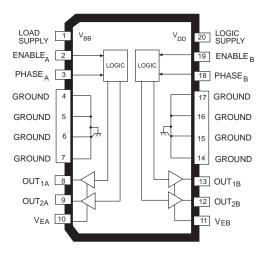
Always order by complete part number:

Part Number	Package		
UDN2993B	16-Pin DIP		
UDN2993LB	20-Lead Wide-Body SOIC		

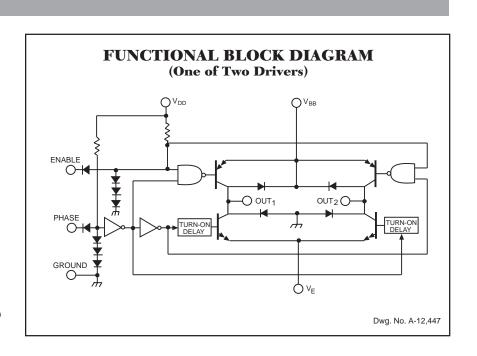


#### 2993 DUAL H-BRIDGE MOTOR DRIVERS

#### **UDN2993LB**

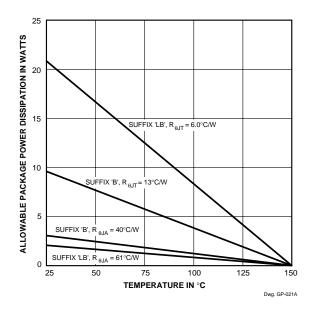


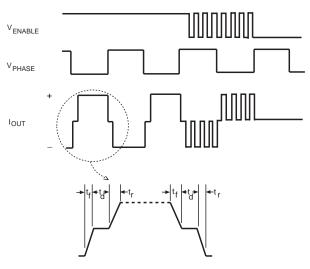
Dwg. No. A-14,340



#### TRUTH TABLE

I	nable nput	Phase Input	Output 1	Output 2
ŀ	ligh	High	Low	High
+	High	Low	High	Low
L	_OW	High	Low	Open
1	_OW	Low	Open	Low





Dwg. No. A-12,448



## **DUAL H-BRIDGE MOTOR DRIVERS**

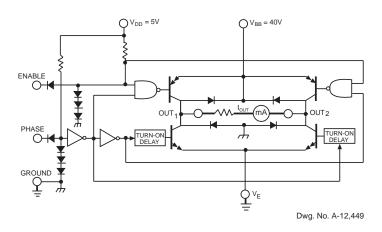
# ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$ , $V_{BB} = 30$ V, $V_{DD} = 5$ V, $V_E = 0$ V, $T_J \le +150^{\circ}C$ Figure 1 (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Output Drivers					•	
Operating Voltage Range	V <sub>BB</sub>		10	_	30	V
Output Leakage Current	I <sub>CEX</sub>	V <sub>ENABLE</sub> = 0.8 V, V <sub>OUT</sub> = V <sub>BB</sub> , Note 2	_	< 1.0	50	μΑ
		V <sub>ENABLE</sub> = 0.8 V, V <sub>OUT</sub> = 0 V, Note 2	_	< -1.0	-50	μΑ
Output Saturation Voltage	V <sub>CE(SAT)</sub>	V <sub>ENABLE</sub> = 2.4 V, I <sub>OUT</sub> = 500 mA	_	1.6	1.8	V
		V <sub>ENABLE</sub> = 2.4 V, I <sub>OUT</sub> = -500 mA	_	1.6	2.0	V
Output Sustaining Voltage	V <sub>CE(sus)</sub>	I <sub>OUT</sub> = ±500 mA, Figure 2, Note 2	30	_	_	V
Motor Supply Current	I <sub>BB(ON)</sub>	V <sub>ENABLE</sub> = 2.4 V, Outputs Open, Note 2	_	1.0	3.0	mA
	I <sub>BB(OFF)</sub>	V <sub>ENABLE</sub> = 0.8 V, Outputs Open, Note 2	_	250	300	μΑ
Source Driver Rise Time	t <sub>r</sub>	I <sub>OUT</sub> = -500 mA	_	75	_	ns
Source Driver Fall Time	t <sub>f</sub>	I <sub>OUT</sub> = -500 mA	_	280	_ [	ns
Clamp Diode Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 500 mA	_	1.6	1.8	V
Control Logic (PHASE or ENA	BLE)	•			•	
Logic Input Current	I <sub>IN(1)</sub>	V <sub>PHASE</sub> or V <sub>ENABLE</sub> = 2.4 V	_	< 1.0	10	μΑ
	I <sub>IN(0)</sub>	V <sub>PHASE</sub> or V <sub>ENABLE</sub> = 0.8 V	_	-200	-300	μΑ
Logic Input Voltage	V <sub>IN(1)</sub>		2.4	_	_	V
	V <sub>IN(0)</sub>		_	_	0.8	V
Logic Supply Current	I <sub>DD</sub>		_	14	20	mA
Turn-On Delay Time	t <sub>pd0</sub>	ENABLE Input to Source Drivers	_	250	_	ns
Turn-Off Delay Time	t <sub>pd1</sub>	ENABLE Input to Source Drivers —		500	_ 1	ns

NOTES: 1. Each driver is tested separately.
2. Test is performed with V<sub>PHASE</sub> = 0.8 V and then repeated for V<sub>PHASE</sub> = 2.4 V.
3. Negative current is defined as coming out of (sourcing) the specified device pin.

#### **TEST FIGURES**

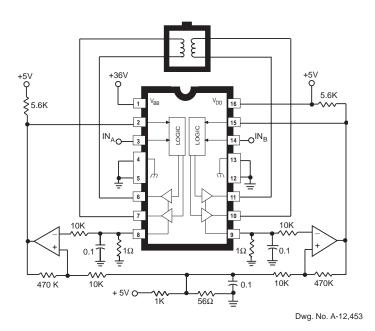
#### FIGURE 1

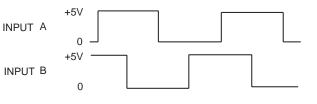


#### FIGURE 2

# 2.4V 2.4V ENABLE PHASE GROUND TURN-ON DELAY Dwg. No. A-12,450

# TYPICAL APPLICATION 2-PHASE BIPOLAR STEPPER MOTOR DRIVE (Chopper Mode)

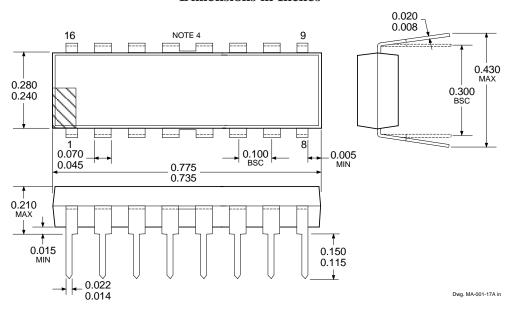




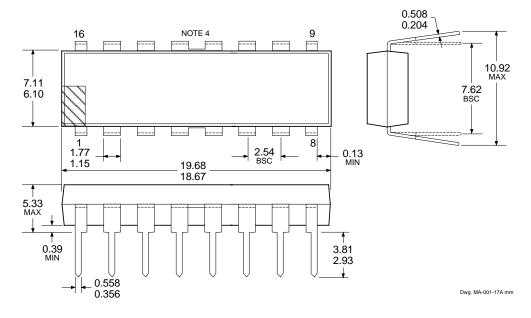
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### **UDN2993B Dimensions in Inches**



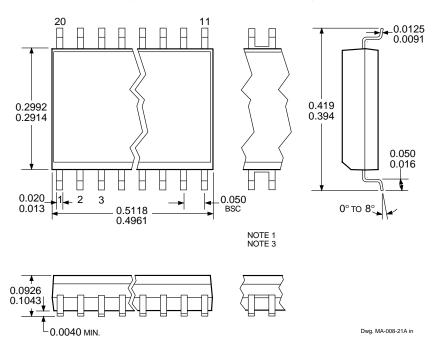
## Dimensions in Millimeters (Based on 1" = 25.4 mm)



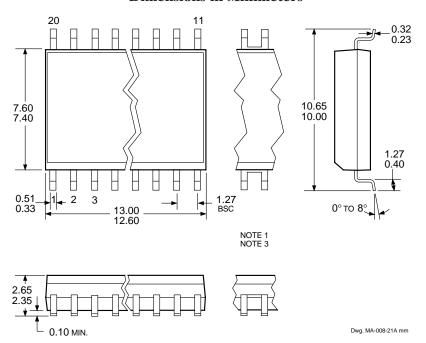
NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

- 2. Lead spacing tolerance is non-cumulative
- 3. Lead thickness is measured at seating plane or below.
- 4. Webbed lead frame. Leads 4, 5, 12, and 13 are internally one piece.

# UDN2993LB Dimensions in Inches (Based on 1 mm = 0.03937")



#### **Dimensions in Millimeters**



NOTES: 1. Webbed lead frame. Leads 5, 6, 15, and 16 are internally one piece.

- 2. Lead spacing tolerance is non-cumulative.
- 3. Exact body and lead configuration at vendor's option within limits shown.



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PWM Current Controlled Dual Full Bridge

PWM Current Controlled Full Bridge

PWM Current Controlled Full Bridge

**Dual Full Bridge Driver** 

### **MOTOR DRIVERS SELECTION GUIDE**

Output Da	4:n.a.a. *	Don't November 4			
		•			
INTEGRATED CIRCUITS FOR BRUSHLESS DC MOTORS					
±2.0 A	45 V	2936 and 2936-120			
10 mA	24 V	3175 and 3177			
20 mA	25 V	3235			
20 mA	25 V	3275			
900 mA	14 V	3625			
400 mA	26 V	3626			
300 mA	60 V	5275			
±900 mA	14 V	8902–A			
±4.0 A	14 V	8925			
±1.0 A	7 V	8984			
INTEGRATED BRIDGE DRIVERS FOR DC AND BIPOLAR STEPPER MOTORS					
±750 mA	45 V	2916			
±1.5 A	45 V	2917			
	±2.0 A 10 mA 20 mA 20 mA 900 mA 400 mA 300 mA ±900 mA ±4.0 A ±1.0 A <b>FOR DC AND</b> II	±2.0 A 45 V 10 mA 24 V 20 mA 25 V 20 mA 25 V 900 mA 14 V 400 mA 26 V 300 mA 60 V ±900 mA 14 V ±4.0 A 14 V ±1.0 A 7 V  FOR DC AND BIPOLAR S ±750 mA 45 V	### TS FOR BRUSHLESS DC MOTORS  ### ±2.0 A		

±1.5 A

±750 mA

±2.0 A

±2.0 A

±1.3 A

±800 mA

±800 mA

45 V

45 V

50 V

50 V

50 V

45 V

30 V

#### OTHER INTEGRATED CIRCUIT & PMCM MOTOR DRIVERS

Unipolar Stepper Motor Quad Driver	1.8 A	50 V	2544
Unipolar Stepper-Motor Translator/Driver	1.25 A	50 V	5804
Unipolar Stepper-Motor Quad Driver	1 A	46 V	7024 and 7029
Unipolar Microstepper-Motor Quad Driver	1.2 A	46 V	7042
Voice-Coil Motor Driver	±500 mA	6 V	8932-A
Voice-Coil Motor Driver	±800 mA	16 V	8958
Voice-Coil (and spindle) Motor Driver	±350 mA	7 V	8984

<sup>\*</sup> Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.
Negative current is defined as coming out of (sourcing) the output.

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2998

3952

3953

3961

3962



<sup>†</sup> Complete part number includes additional characters to indicate operating temperature range and package style.