

Universal Serialization controller

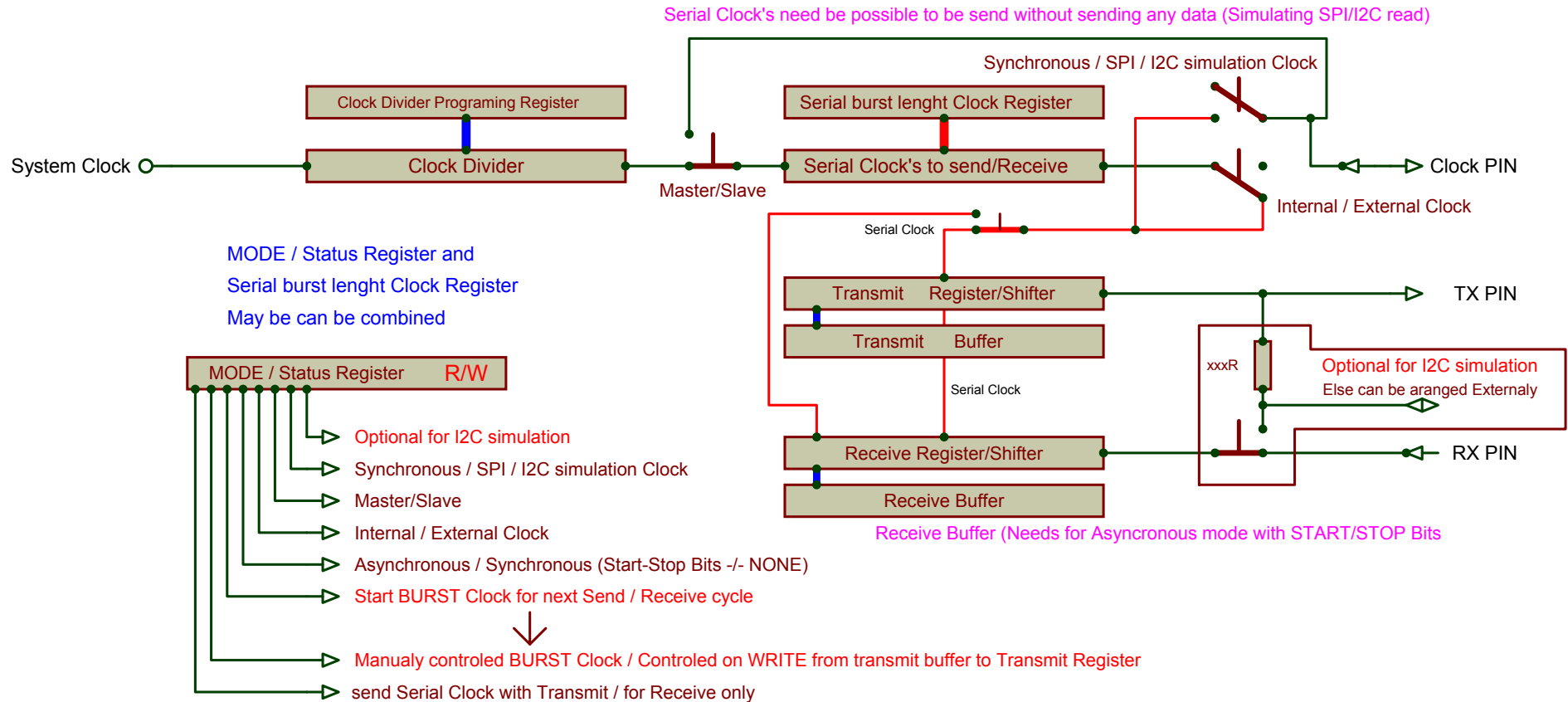
(all Registers are 32 Bit's) -- In some maybe not all Bit's used

If it is build with fully Asynchronous Serializer that insert automatically SRART / STOP for 8/16/32 Bits lenght.

That needs Second Mode that dno't insert any EXTRA Bits for all types of Synchronous communication (Burst lenght programmable MODES of 1 to 32 Bits)

In MODE / Status Register

That construction give possibility to simulate all types Serial communication's (Standard-Extended Asynchronous/Synchronous - SPI and I2C)



(I know it need some more thinking before it is usable but give much possibility's)

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