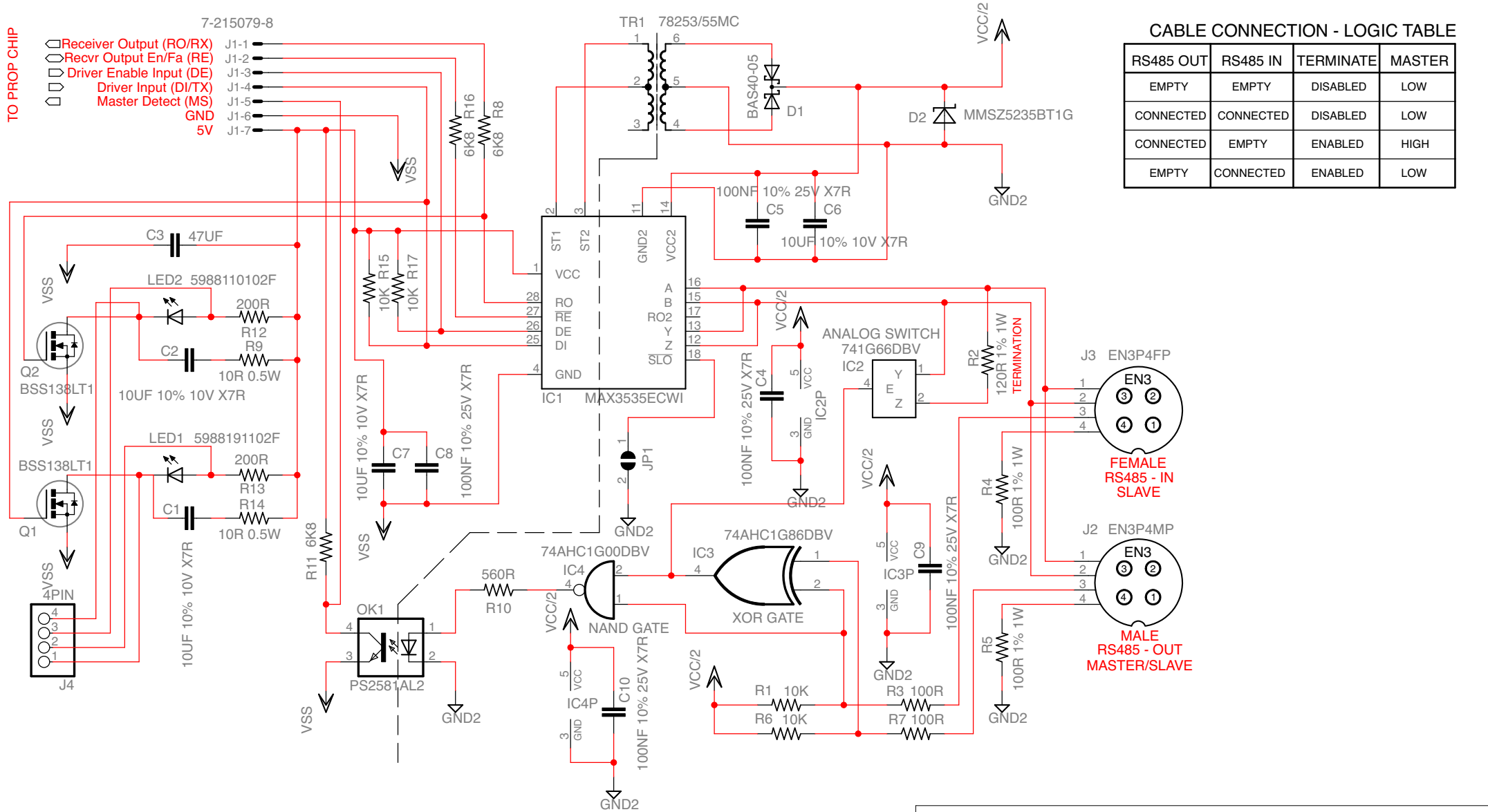


TO PROP CHIP

7-215079-8

- ◻ Receiver Output (RO/RX)
- ◻ Recvr Output En/Fa (RE)
- ◻ Driver Enable Input (DE)
- ◻ Driver Input (DI/TX)
- ◻ Master Detect (MS)
- ◻ GND
- ◻ 5V

J1-1
J1-2
J1-3
J1-4
J1-5
J1-6
J1-7



CABLE CONNECTION - LOGIC TABLE

RS485 OUT	RS485 IN	TERMINATE	MASTER
EMPTY	EMPTY	DISABLED	LOW
CONNECTED	CONNECTED	DISABLED	LOW
CONNECTED	EMPTY	ENABLED	HIGH
EMPTY	CONNECTED	ENABLED	LOW

AUTO TERMINATION - GALVANIC ISOLATED - MASTER/SLAVE DETECT 5V

THIS SCHEMATIC IS NOT TESTED!!!

TITLE: RS485-INTERFACE BOARD REV-P

Document Number:

REV:

Date: 8/07/09 10:30 AM

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