



- P?? Receiver Output (RX) J1-1
- P06 Recvr Output En/Fault J1-2
- P07 Driver Enable Input (DE) J1-3
- P25 Driver Input (TX) J1-4
- P24 Master Detect J1-5
- TO PROP CHIP GND J1-6
- 5V J1-7

CABLE CONNECTION - LOGIC TABLE

RS485 OUT	RS485 IN	TERMINATE	MASTER
EMPTY	EMPTY	DISABLED	LOW
CONNECTED	CONNECTED	DISABLED	LOW
CONNECTED	EMPTY	ENABLED	HIGH
EMPTY	CONNECTED	ENABLED	LOW

AUTO TERMINATION - GALVANIC ISOLATED - MASTER/SLAVE DETECT 5V
THIS SCHEMATIC IS NOT TESTED!!!

TITLE: RS485-INTERFACE BOARD REV-M	
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