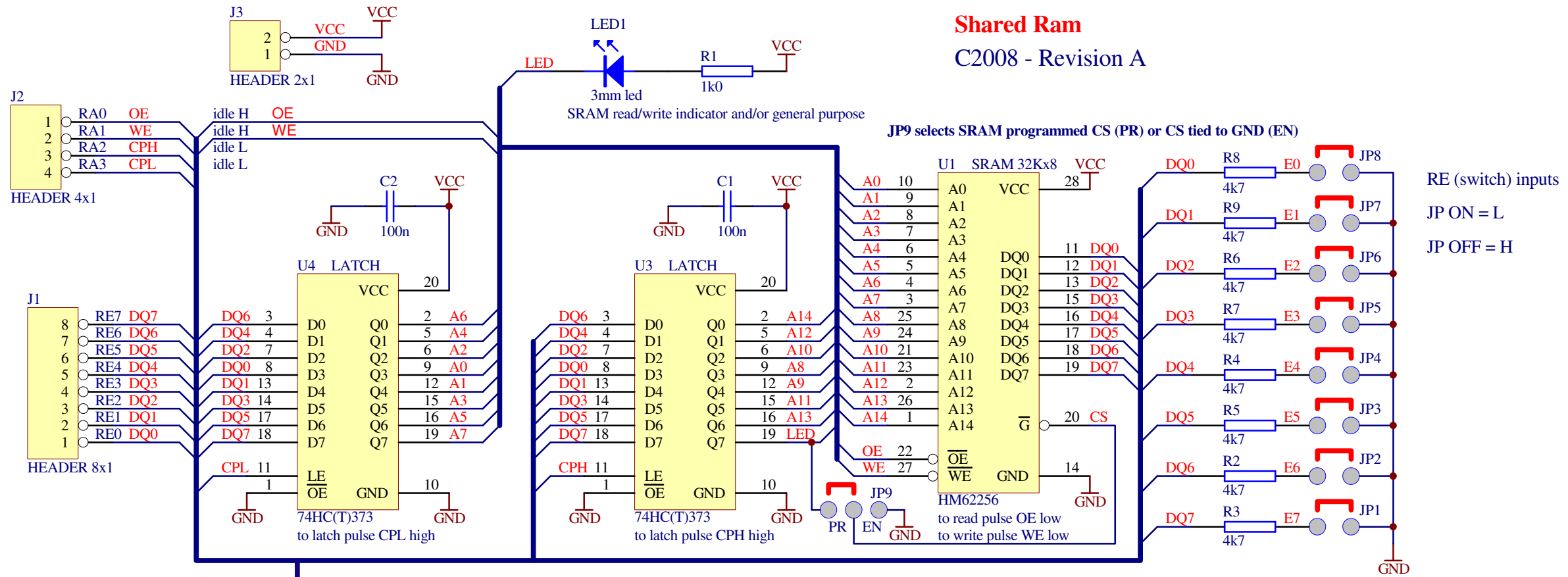
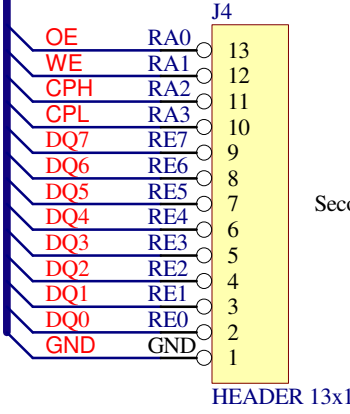


**Shared Ram**  
C2008 - Revision A



RE (switch) inputs  
JP ON = L  
JP OFF = H



Second MCU accessing ram

Protocol: I2C-like using CPL and CPH  
All pullups enabled, make pin input for high, make pin output for low  
Start condition: CPL goes low while CPH is high  
Stop condition: CPH goes high while CPL is high  
Idle state: RA input, RE input

arbitration:  
CPL low (start condition)  
Wait random time  
CPL high  
CPL high  
if CPL=low then arbitration lost (exit, wait for stop, then try again)  
RE = addrH  
CPH low (this latches A8-A14,CS)  
RE = addrL  
CPL = low (this latches A0-A7)

for write:  
RE = data  
WE low  
WE high  
RE input

for read:  
RE input  
OE low  
data = RE  
OE high

for read and write:  
CPL high  
CPH high (stop condition)  
idle: