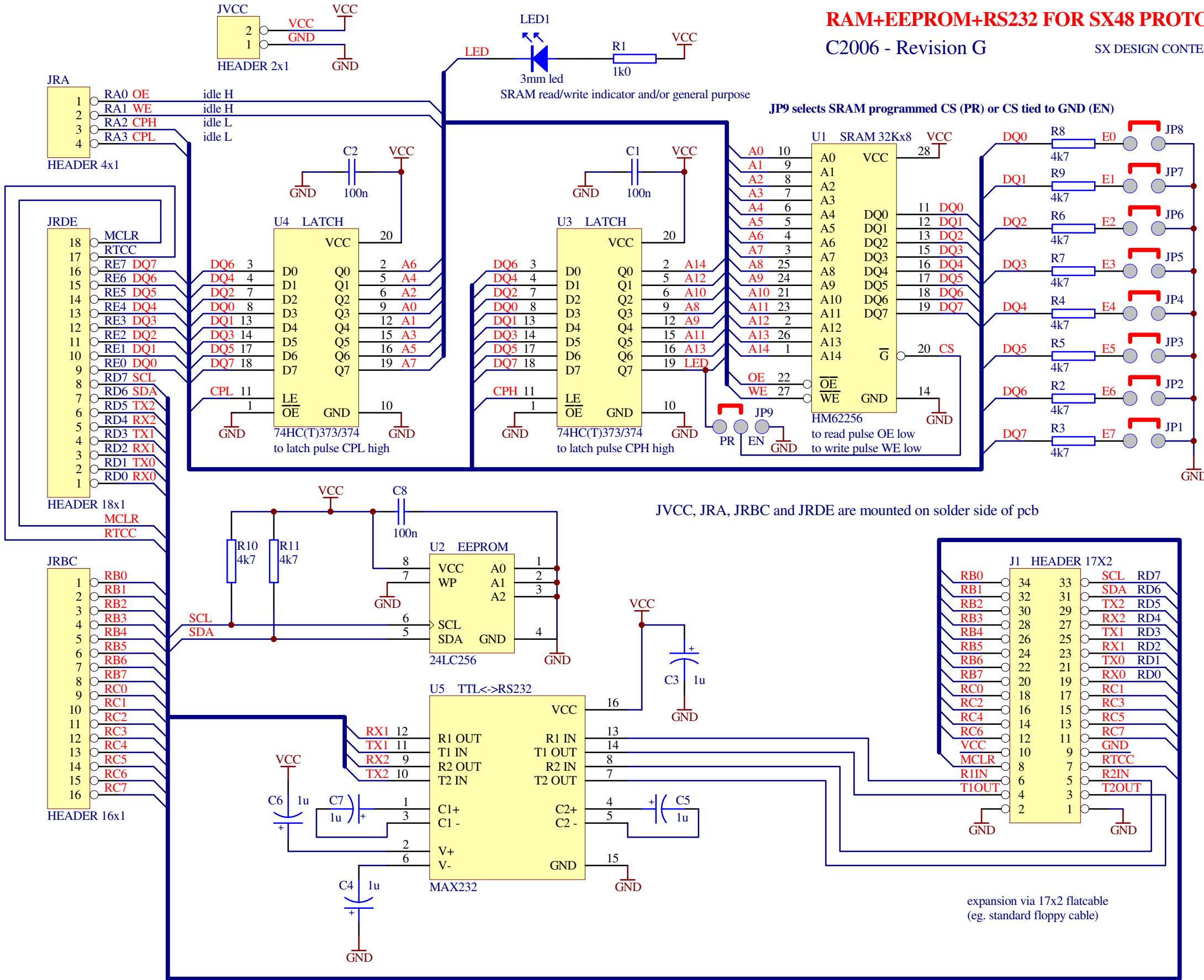


RAM+EEPROM+RS232 FOR SX48 PROTOBOARD

C2006 - Revision G

SX DESIGN CONTEST 2006/2007



JVCC, JRA, JRBC and JRDE are mounted on solder side of pcb

ALTERNATIVE MOUNT J1 when MCLR and RTCC are not required

expansion via 17x2 flatcable (eg. standard floppy cable)

expansion via 13x2 flatcable RS232 via 3pin headers

RE (switch) inputs
JP ON = L
JP OFF = H

JP9 selects SRAM programmed CS (PR) or CS tied to GND (EN)