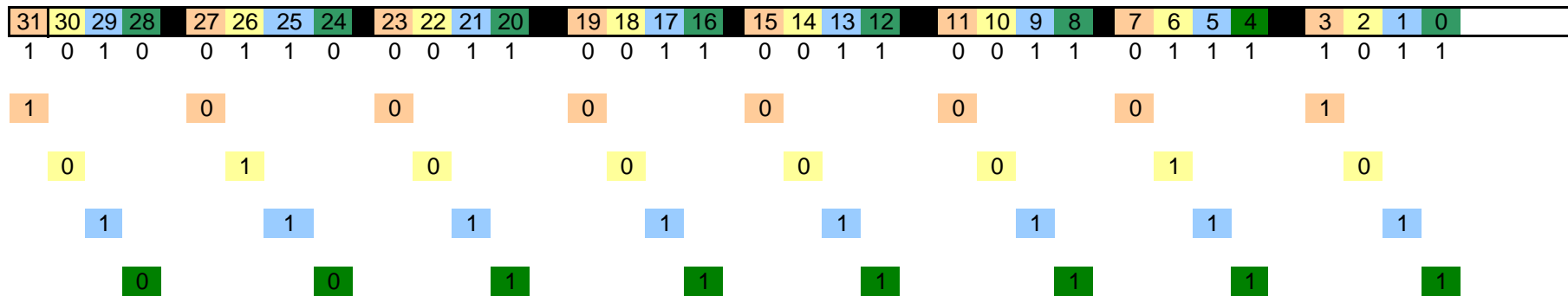


AD values for example

R T Nollet www.nollet.com.au

- AD1 129
- AD2 66
- AD3 255
- AD4 63

The long var contains 4 off 8bit a/d conversion value's



ALL CS signals together. (to one output from Propeller)
 All CLK signals together (to another single output from the Propeller)
 All data from A/D chip to separate inputs... (to 4 inputs on Propeller)

So total I/O count used to sample 4 a/d chip is 6
 ADC 0831 or TLC0831 8 bit a/d converter chip.

Each block(4 bits) separated by a black cell is data collected by the one yes 1 clk pulse.
 Some people call this INTERLEAVED data collecting.
 This long can be send to another COG via 1 single HUB sync as well..
 Using the WRLONG and RDLONG instructions..
 cheers Ron Nollet