

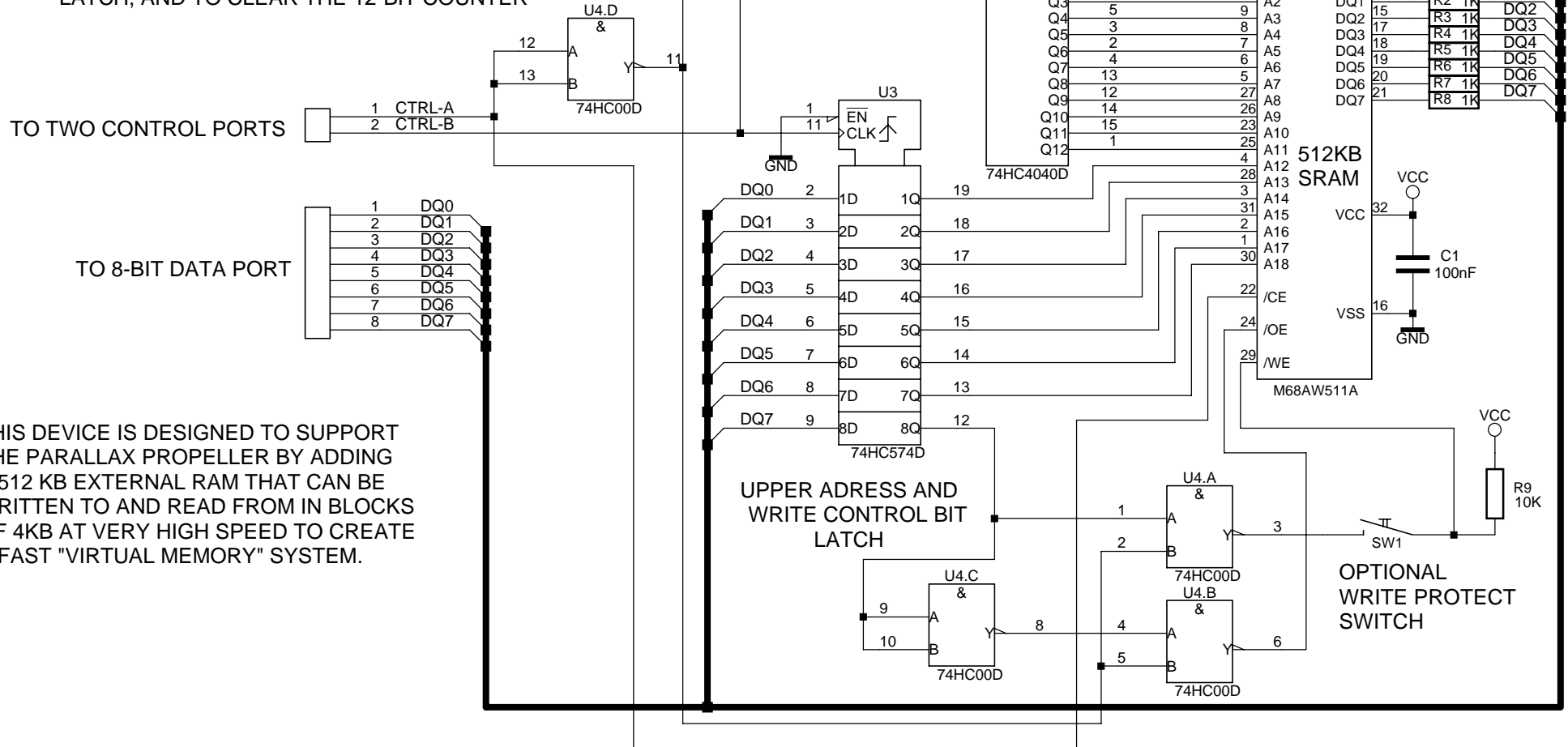
512 KB SRAM MEMORY EXPANSION FOR THE PARALLAX PROPELLER

CTRL-A NORMALLY HIGH, PULSE LOW TO WRITE OR READ TO OR FROM THE RAM. AFTERWARD THE 12 BIT COUNTER WILL BE INCREMENTED TO POINT TO THE NEXT LOCATION

CTRL-B NORMALLY LOW, PULSE HIGH TO LATCH NEW UPPER 7 BITS OF ADDRESS AND THE CONTROL BIT TO THE LATCH, AND TO CLEAR THE 12-BIT COUNTER

4K BLOCK LOWER ADDRESS COUNTER

CURRENT LIMIT RESISTORS IN CASE OF BUSCONFLICTS



TO 8-BIT DATA PORT

UPPER ADDRESS AND WRITE CONTROL BIT LATCH

OPTIONAL WRITE PROTECT SWITCH

THIS DEVICE IS DESIGNED TO SUPPORT THE PARALLAX PROPELLER BY ADDING A 512 KB EXTERNAL RAM THAT CAN BE WRITTEN TO AND READ FROM IN BLOCKS OF 4KB AT VERY HIGH SPEED TO CREATE A FAST "VIRTUAL MEMORY" SYSTEM.

WHETHER DATA IS WRITTEN TO OR READ FROM THE RAM DEPENDS ON THE STATUS OF THE UPPER BIT OF THE LATCH
 IF Q8 = 0 THEN DATA IS READ
 IF Q8 = 1 THEN DATA WILL BE WRITTEN

(C) 2007 M.C.J. DE JONG
 QUIRINEGANG 121 2719 CH ZOETERMEER THE NETHERLANDS.

JANUARY 22, 2007

NON COMMERCIAL USE OF THIS INFORMATION IS PERMITTED