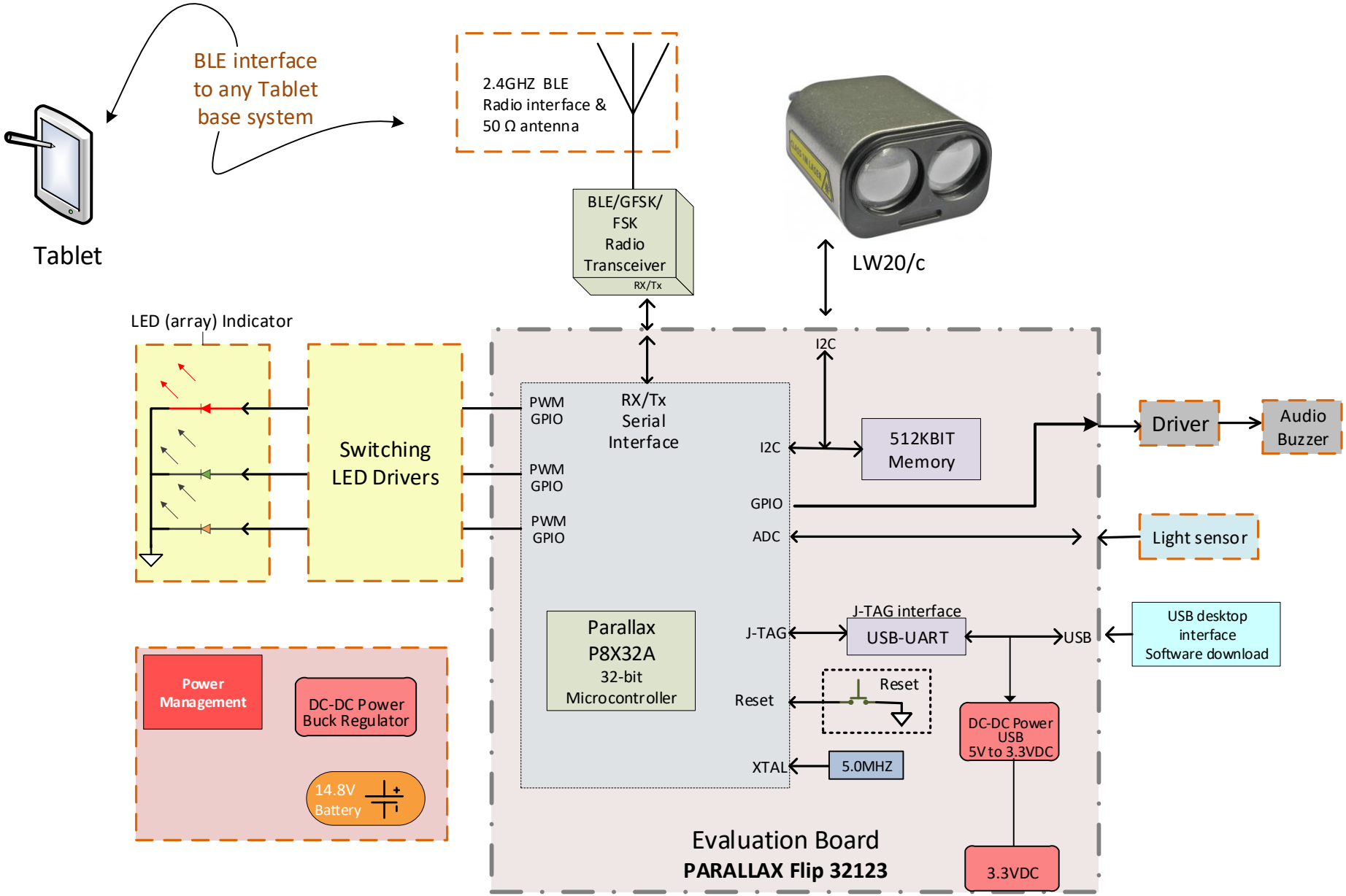
High Level System Hardware Block Diagram

Preliminary TML Concept
DRAFT Rev 0.1



Confidential

May 14, 2020