At this point we should be able to get in and out of PASM and do some math and create and target specific array cells.

We are now going to revisit those objects and create subroutines with each one.

Let's start with the counting program that counts up from zero.

```
1
3
5 CON
9
10 var
   long count
14 obj
16 pst:"parallax serial terminal"
15
18 pub main
20 pst.start (115000)
   21
23 cognew (@asm,@count)
24
25
     repeat
     pst.dec(count~) post clear p 157
26
     pst.newline
28
29
     waitcnt(clkfreg +cnt)
```

Figure 1

```
30 dat
32 asm
                  org
     mov addr, par
loop add value,#1 'counting variable
wait rdlong prev, addr wz 'what is in par??
if_nz jmp #wait 'if the value in
'par is zero continue to next command
'if the value in par "addr" has not been cleared
'meaning the value that was put in "value" from
'addr which has the address of par "parameter"
34
35
36
38
39
40
41
42
43
44
                   wrlong value, addr
                      now write the value to the addr which has been assigned
                    'the same address as par and where the address of count in
45
46
                      memory where the spin program can read it then jump back
47
                    to the top of the loop and continue after the variable
48
                       called count has been cleard to zero
49
                   jmp #loop
52 addr long Ø
53 value long 0
54 prev long 0
```

Figure 2

Now we are going to add three lines of code, the code definitions are as follows as seen on lines 44,45 and 61 on the next listing:

CALL

Instruction: Jump to address with intention to return to next instruction.

CALL #Symbol

Result: PC + 1 is written to the s-field of the register indicated by the d-field.

Symbol (s-field) is a 9-bit literal whose value is the address to jump to. This field
must contain a DAT symbol specified as a literal (#symbol) and the corresponding
code should eventually execute a RET instruction labeled with the same symbol plus a
suffix of "_ret" (Symbol_ret RET).

Explanation

CRLL records the address of the next instruction (PC + 1) then jumps to Symbol. The routine at Symbol should eventually execute a RET instruction to return to the recorded address (PC+1; the instruction following the CRLL). For the CRLL to compile and run properly, the Symbol routine's RET instruction must be labeled in the form Symbol with "_ret" appended to it. The reason for this is explained below.

Propeller Assembly does not use a call stack, so the return address must be stored in a different manner. At compile time the assembler locates the destination routine as well as its RET instruction (labeled *Symbol* and *Symbol_*ret, respectively) and encodes those addresses into the CRLL instruction's s-field and d-field. This provides the CRLL instruction with the knowledge of both where it's going to jump to and exactly where it will return from.

At run time the first thing the CRLL instruction does is store the return address (PC+1) into the location where it will return from; the " $Symbol_{ret}$ RET" instruction location. The RET

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3: Assembly Language Reference - CALL

instruction is really just a JMP instruction without a hard-coded destination address, and this run-time action provides it with the "return" address to jump back to. After storing the return address, CALL jumps to the destination address; Symbol.

The diagram below uses a short program example to demonstrate the CRLL instruction's runtime behavior; the store operation (left) and the jump-execute-return operation (right).

Figure 3-1: Run-time CALL Procedure

Store operation Jump, execute and return operation -(2) call #Routine <next instruction call #Routine (PC-H) 1 <more code> Routine Routine <more code> Ŗ(4 Jump Routine_ret ret PC+1 ret PC+1 -Routine_ret 90Hintel

In this example, the following occurs when the CALL instruction is reached at run time:

- The cog stores the return address (PC+1; that of <next instruction>) into the source (s-field) of the register at Routine_ret (see left image).
- The cog jumps to Routine (see right image).
- ③ Routine's instructions are executed, eventually leading to the Routine ret line.
- ③ Since the Routine_ret location contains a RET instruction with an updated source (s-field), which is the return address written by step 1, it returns, or jumps, back to the <next instruction>line.

CALL - Assembly Language Reference

This nature of the CALL instruction dictates the following:

- The referenced routine must have only one RET instruction associated with it. If a
 routine needs more than one exit point, make one of those exit points the RET
 instruction and make all other exit points branch (i.e., JMP) to that RET instruction.
- The referenced routine can not be recursive. Making a nested call to the routine will
 overwrite the return address of the previous call.

CRLL is really a subset of the JMPRET instruction; in fact, it is the same opcode as JMPRET but with the i-field set (since CRLL uses an immediate value only) and the d-field set by the assembler to the address of the label named Symbol_ret.

The return address (PC + 1) is written to the source (s-field) of the Symbol_ret register unless the NN effect is specified. Of course, specifying NR is not recommended for the CALL instruction from that turns into a. NP or BFT instruction

RET

Instruction: Return to previously recorded address.

JMP

Instruction: Jump to address.

JMP (#) Address

Figure 3

30 dat	
31	{{First subroutine. we are going to add three lines. First line
32	to add is call #wait, this tells the program to go and find a set of code named
33	wait". at the bottom of the wait subroutine the following is added
34	"wait ret ret ", this signals the end of the subroutine and to jump
35	back to the next line of code after the "call".
36	jmp #loop is added as the next line of code to execute which sends the
37	code back to execute an "add" directive.}}
38 asm	org
39	
40	mov addr, par
41 loc	pp add value,#1 counting variable
42	
43	

Figure 4

44		call #wait `<< <add< th=""></add<>
45		imp #loop '<< <add< th=""></add<>
46	wait r	cdlong prev addr wz what is in par??
17	ware .	if provide the second of the s
71	-	ri_uzjmp #warcri the varue ru
48		par is zero continue to next command
49		if the value in par addr has not been cleared
50		meaning the value that was put in "value" from
51		addr which has the address of par "parameter"
52		
53		releng value, addr
		'and write the walks the side which has been ended
04		now write the value to the addr which has been assigned
55		the same address as par and where the address of count in
56		memory where the spin program can read it then jump back
57		to the top of the loop and continue after the variable
58		called count has been cleard to zero
59		imp #loop
60		3mp - 1200p
61	wait not	pot CCCPDD
01	marcler	rec sonoo
02		
63		
64	addr long (0
65	value long	0
66	prev long	0

Figure 5

Adding line 44 will call the subroutine named "wait". The routine will execute the code that is listed there. Upon completion of the code routine the "ret" command will send the code back to the next line of code after the "call" in this case it is a "jmp" meaning a jump to the address listed in the jmp command, which in this case is "loop" which is where the "add" command will add 1 to the value. You should see this:



Now, let's get a little deeper and make a couple of other changes. The above code will be modified and will have two subroutines. Figure 7, line 41, add the "repeat_" label with associated code through line 43. Modify lines 44 and 45 as indicated.

These modifications should result in an endless loop that is incrementing a variable. We are going to call the loop routine that does the addition, then call the wait routine that causes a lockstep between PASM and SPIN, then jump back to repeat the loop of calls.

```
5 CON
 6 _clkmode = xtal1 + pll16x
7 _xinfreq = 6_250_000 MY BOARD AT 100MHZ
 10 var
    long count
  obj
  pst:"parallax serial terminal"
 8 pub main
 0 pst. start (115000)
     waitcnt(clkfreq*5 +cnt) hold two sed to open the
       serial terminal and enable it
 3 cognew (@asm,@count)
       repeat
        pst.dec(count~) post clear p 157
        pst.newline
28
29
        waitcnt(clkfreq +cnt)
```

```
Figure 6
```



Figure 8

Since we now have two subroutines you should see this as seen in figure 9:



Figure 9

O.K. we are going to move on to the multiplication, addition, and subtraction code and create subroutines. In order to do this we also have to understand a difference from the continuous addition and doing a single multiplication etc. Thanks to the forums and David Carrier at Parallax, who both pointed out again that we have to stop the cog otherwise the results will be screwed up. I was wondering why I got zeros.

So, let's look at the code. Please refer to chapter one figures 28,29 and 30 and compare with chapters 10 through 13 in this chapter.

You will see the addition on line 62 execution of "multiply_ret ret", line 55 multiply label and lines 51 through 55.

We will now have a subroutine that executes and stops the cog after execution. The result will now be ready for the spin method to print the results.

The code is presented on the next page for your review.

The next code examples will be multiplication, division and

Figure 10

 31

 32

 33

 34

 35

 36

 37

 38

 39

 mov temp_var, par

 mov gat

 41

 rdlong x_var, temp_var

 11

 rdlong x_var, temp_var

 12

 13

 14

 rdlong x_var, temp_var

 15

 16

 17

 18

 19

 19

 10

 11

 11

 12

 13

 14

 15

 16

 17

 18

 19

 19

 19

 10

 10

 10

 10

 10

 10

 10

 10

 10

 10

 10

 10

 10

 10

 10

```
Figure 11
```

50	
51	call #multiply
52	call #writer
53	cogid cogname
54	cogstop cogname
55 multiply	shl x var,#16 'get multiplicand into x[3116]
56	mov t.#16 'ready for 16 multiplier bits
57	shr y var,#1 wc 'get initial multiplier bit into c
58 : loop	if c add y var,x var wc if c set, add multiplicand to product
59	rcr y var,#1 wc put next multiplier in c, shift prod.
60	djnz t,#:loop 'loop until done
61	call #writer `<<< <add< th=""></add<>
62 multiply_ret	ret 'return with product in y[310] 'this would be a subroutine
63	' when used in a program
64	ˈcall #writer ˈ<<< <add< th=""></add<>
65	
66 writer {<< <add}< th=""><th>wrlong y_var, product_var `write the product from y[310] to the</th></add}<>	wrlong y_var, product_var `write the product from y[310] to the
67	product variable for the top object
68 writer_ret ret	:< <add< th=""></add<>
69	
70 temp_var res 1	
71 x_var res 1	
72 y_var res 1	
73 product_var res 1	
74 t res 1	
75 cogname res 1	

Figure 12

product:27 product:27 product:36 product:27



```
1 CON
    _clkmode = xtal1 + pll16x
_xinfreq = 5_000_000
                                       QUICKSTART 80 MHZ NORMAL CRYSTAL
5 var
6
7
8
9
                             VARIABLE IN THE PAR ADDRESS TO BE PASSED
     long dividend
      long divisor
long quotient
     long remainder
11 obj
   pst : "parallax serial terminal"
12
13
 L4 pub main
      dividend := 35
15
16
     divisor := 3
pst.start(115200)
     19
20
23
24
25
26
     pst.str(string("quotient:"))
     pst.dec(quotient)
pst.newline
pst.str(string("remainder:"))
pst.dec(remainder)
27
28
29
     pst.newline
```

Figure 14

32 dat 33 dat 33 d(NOTE: I have removed three mov commands as I have been shown that they are unnessary 34 each "add tempvar,#4" points to the next variable. I got that from the NUTS AND VOLTS 5 and appears that that may not be necessary.}) 36 37 asm org 38 mov tempvar, par get the par address into the temporary variable rdlong x, tempvar read the value into the dividend 39 40 41 42 add tempvar, #4 imove over to the next long to get the divisor variable rdlong y, tempvar i read the value of the divisor into the variable add tempvar, #4 imove over to the next long to get the quotient address call #divide '<<</pre> 43 44 45 call #duvide <<<<>HUU urlong remaindervar,tempvar cogid cogname '<<<<<<RDD cogstop cogname '<<<<<RDD Divide x[31.0] by u[15..0] (u[16] must be 0) on exit, quotient is in x[15..0] and remainder is in x[31..16] 46 49 50 51 52 divide shl y,#15 54 :loop 56

Figure 15

```
state of the state of the
```

79 cogname res 1

Figure 16

quotient:11 remainder:2



30 dat
31
32 asm org
33
4 mov tempvar, par 'get the address of x from par
35 mov xwar, tempvar 'assign the address to the xwar in pasm 'DISABLE
36 rdlong xwar, tempvar 'read the value that is in x
37 add tempvar, "# 'move over one long to get y's address
38 'mov ywar, tempvar 'assign that address to ywar 'DISABLE
39 rdlong ywar, tempvar 'read the value that is in y
40 add tempvar, "# 'move over one long to get the address of product
41 mov productvar, tempvar 'assign the address to product or 'DISABLE
42 call #adder '<<<<ADD
44 call #uriter '<<<<ADD
45 cogid cogname '<<<<ADD
46 cogid cogname '<<<<ADD
47 adder add xwar, ywar 'add x and y together answer will be in x ;<<ADD LABLE
48 adder_ret ret '<<<<ADD
49
50
51 writer wrlong xvar, productvar 'write x into the product variable and print
52 writer_ret ret '<<<<ADD
53 tempvar long 0
54 tempvar long 0
55 kwar long 0
56 juan 00
57 productvar long 0
58 cogname res 1 '<<<<ADD
59 cogname res 1 '<<<<ADD
50 juan 100
50 juan 100
51 juan 100
5

product:75

23	pub main		
24	datavar:= 30	assign a value to datavar	
25	subvar := 12		
26			
27	pst.start(115000)	start the serial terminal object	
28			
29	waitcnt(clkfreg*5 +cnt)	'hold five sec to open the	
30			
31	coonew (@asm.@datavar)	open a new coo for pasm, where it starts "asm" and	
32	,	the address of the first variable	
33	waitcot(clkfreq+cot)	bold for a second	
34	war contraction of oney		
35		print coutine	
36		print rodtine	
37			
38	paciaci (aci ing (readica	• //	
30	not des (answanyan)		
10	pst.uec(answervar)		
40	pst.newilne		
4 L			

Figure 18



Figure 19



Figure 20



Figure 21